Leonard Curtis

CHIEF TECHNICAL ARCHITECT
REAL TIME EMBEDDED SOFTWARE SYSTEMS
TRANSFORMING PRODUCT CONCEPTS INTO PROFITS

Exceptional technical, analytical, and engineering qualifications. Demonstrated achievement in delivering multi-million dollar projects on time and within budget. Unique understanding of customer needs/market demands, with proven ability to develop customer-focused products that drive/sustain long-term revenue and profits. Resourceful, hands-on troubleshooter who finds real-world answers. Enjoys the hands-on experience and willing to roll up his sleeves and work relentlessly when duty calls. Previously held Top Secret Security Clearance at Martin-Marietta.

EXPERIENCED IN:

- Structured Development Methodologies
- Configuration Management and Source Code Control (VSS, PVCS, SCCS)
- Calibration and Test Systems
- Electro-Mechanical Systems
- Creative application of off-the-shelf products and components

- Kernel Development
- Socket Programming
- Memory Management
- Multi-threaded Programming
- Object-Oriented Analysis, Design, and Programming
- GUI Design and Development
- Message Queue Programming

LEADERSHIP

- Envisioning practical implementations of product concepts and technologies.
- Integrating the latest resources and equipment; incorporating new technologies quickly and proficiently.
- Evaluating/managing projects from a technical, operational, logistical, and financial perspective.
- Optimizing processes and operations to realize cost reductions.
- Developing/sharing best practices to increase effectiveness, productivity, and customer satisfaction.
- Managing stress, unpredictable workloads, conflicting deadlines, and interruptions.

KNOWLEDGE AND EXPERTISE

Platforms	Wintel Platforms (Pentium and X86), UNIX Platforms (68000), Mac, Minicomputers
Operating Systems	Windows-2000/NT, UNIX, RTOS, MSDOS
Programming Languages	Assembly Language, C, C++, Delphi, Pascal, Basic, FORTRAN
Libraries & APIs	MFC, Win32, SCSI, ASPI
Development Tools	Microsoft Visual C++, Borland Delphi IDE, HTML, MSDOS Cross Tools
Hardware	Logic Analyzers, Scopes, In-Circuit Emulators, SCSI Analyzers
Microcontrollers	68HC08, 680X, 680X0, Intel 80XX
Protocols	TCP/IP, Fibre-Channel, PCI Bus Protocol
Device Drivers	Custom PCI-base Fibre Channel Driver, UNIX Serial Driver, VAX Driver

PROFESSIONAL EXPERIENCE

Managed programs from initial concept and proposal preparation through system architecture definition, design, test/debug, deployment, and final client acceptance. Architected, designed, developed, tested, and integrated complex embedded software for system command, hardware control, and device control. Provided engineering guidance to design/production teams and influenced client presentations and contract negotiations. Led cross-functional teams of software/electrical/mechanical engineers throughout entire project cycle. Managed relationships with project/product managers, production managers, and plant managers at deployment sites worldwide.

PROJECTS HIGHLIGHTS AND ACHIEVEMENTS

- Co-Inventor – U.S. Patent numbers 6,012,738, 6,231,427, and 6,123,567 for Components of Bi-Directional Sweeping, CRT-based Photographic Print Engine.

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- Architected, designed, and developed the second generation Fotoprint core print engine used in:

Product	Application
E.Motion-408	Latest photographic mini lab used in one hour photo shops.
RP85 Print Engine	Used by CEWE Color, Inc., the largest independent photofinisher in Europe.
SMI1200 Print Engine	Marketed by San Marco Imaging in the Netprinter, deployed in nationwide retail portrait studios and school photography programs.
Mileca Roll-to-Roll Printer	High capacity printer used in publishing and school photography programs.
Nori12NT Print Engine	Core of the Noritsu photographic mini lab deployed in one hour photo shops.

PREVIOUS NOTABLE PROJECTS

- Led a team in the development/test of 11 different print engine products based on the first generation Fotoprint print engine. Defined/developed an easily adaptable architecture to meet differing customer requirements for product applications. Print engine was developed using T134010 microprocessor-based C and assembly.
- Designed/developed microprocessor and minicomputer software to control 250 Heliostats in a proof-of-concept project that led to a 2048 Heliostat pilot project built in Barstow, CA. Project received much acclaim and was a feature story in the Rocky Mountain News.
- Directed a team of 5 software and 3 hardware engineers in the design/development/installation/training of a reactor-trip monitoring data acquisition system for Duke Power. The software, developed in C and assembly, ran on 7 680X0-based UNIX platforms connected by Ethernet using a distributed UNIX OS.
- Designed, developed, and installed electric power grid control software for the Nashville Electric Service and Los Angeles Department of Water and Power. Developed/delivered training at each site.

PROFESSIONAL HISTORY

Sierra Imaging / Great Imaging , Englewood, CO **June 1995 – January 2003**

Sierra Imaging, Inc., manufacturer of FotoPrint digital color printers was formed when employees purchased Imaging Products from Metrix in 1995. Great Imaging purchased Sierra Imaging in 1999 and declared Chapter 7 bankruptcy in January 2003.

Partner, Lead Software Engineer/Principal System Architect, Second Generation Fotoprint (1997 – 2003)
Lead Software Engineer/Software Architect, Fotoprint Product Development (1995 – 1997)

Metrix / Test Instruments Division, Englewood, CO **March 1983 – June 1995**

Lead Software Engineer and Software Architect
Fotoprint Print Engine and Printers (1992 – 1995)
Dry Silver Color Printer (1988 – 1992)
Honeywell H.TMS Data Acquisition System (1983 – 1988)

Lockheed Aerospace, Senior Software Engineer

Grumman Software Engineer

EDUCATION

B.S. Computer Science, University of Pennsylvania, State College, PA

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